

Electronic Acknowledgement Receipt

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Title of Invention:	SYSTEM AND METHOD FOR STATISTICAL TIMING ANALYSIS OF DIGITAL CIRCUITS
First Named Inventor/Applicant Name:	Chandramouli Visweswariah
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		YOR92003034US1_312Ament.pdf	80594 9b4ae3cd8ea21e93c1390271e27da3ff0ba70295	yes	8

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Specification	1	1
	Claims	2	7
	Applicant Arguments/Remarks Made in an Amendment	8	8
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